

Electronic Acknowledgement Receipt

EFS ID:	2795685
Application Number:	10711493
International Application Number:	
Confirmation Number:	5492
Title of Invention:	A METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR GENERATING AND VERIFYING ISOLATION LOGIC MODULES IN DESIGN OF INTEGRATED CIRCUITS
First Named Inventor/Applicant Name:	Bhanu KAPOOR
Customer Number:	72875
Filer:	Kelly G. Hyndman/Mussie Beyene
Filer Authorized By:	Kelly G. Hyndman
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment		no			
File Listing:					
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	Q82851_PTO85.pdf	67806	no	1
			8a099b46a69a3c1b593f96cb78104854733f70014		
Warnings:					
Information:					

2	Fee Worksheet (PTO-06)	fee-info.pdf	8373 <small>cb07b39c11e2960ba9d9eb9222569684d77e6f3</small>	no	2
Warnings:					
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Total Files Size (in bytes):			76179		
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